

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the application of:

Masato TAKITA et al

Serial No.: 09/046,671

Filed: March 24, 1998



Group Art Unit: 2823

Examiner: K. Eaton

For: SEMICONDUCTOR DEVICE AND METHOD FOR FABRICATING THE SAME

RESPONSE TO RESTRICTION REQUIREMENT

Assistant Commissioner for Patents
Washington, D.C. 20231

September 17, 1999

Sir:

In response to the Official Office Action dated August 30, 1999, the applicants hereby elect to have further prosecution in this application be given on the invention of Group I, drawn to the semiconductor device, currently claimed in Claims 1-7.

The applicants hereby reserve the right to file one or more divisional applications on the non-elected invention.

Accordingly, an early examination and a favorable action on the merits on Claims 1-7 are respectfully requested.

In the event that this paper is not timely filed, the applicants hereby petition for an appropriate extension of time. The fee for this extension may be charged to Deposit Account No. 14-1060, along with any other additional fees which may be required with respect to this paper.

Respectfully submitted,
NIKAIDO, MARMELESTEIN, MURRAY & ORAM LLP

George E. Oram, Jr.
Attorney for Applicants
Reg. No. 27,931

Atty. Docket No. P8077-8003
Metropolitan Square
655 15th Street, N. W.
Suite 330 - G Street Lobby
Washington, D. C. 20005-5701
Tel: (202) 638-5000
GEO:lad